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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application Number	10/062,995
				Filing Date	1/31/02
				First Named Inventor	Steven Teig, et al.
				Group Art Unit	2825
				Examiner Name	Rossoshnek, Yelena
Sheet	1	of	4	Attorney Docket Number	SPLX.P0105

U.S. PATENT DOCUMENTS						
Examiner* Initials	Cite No.	U.S. Patent Document Number Kind Code* (if known)		Date of Publication MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
HR	1.	2001/0003 843	A1	06-14-2001	Scepanovich et al.	
HR	2.	2002/0182 844	A1	12-05-2002	Igarashi et al.	
HR	3.	2003/0053 99	A1	01-02-2003	Igarashi et al.	
HR	4.	2003/0025 205	A1	02-06-2003	Shively et al.	
HR	5.	4,615,011		09-30-1986	Linsker	
HR	6.	4,673,966		06-16-1987	Shimoyama	
HR	7.	4,782,193		11-01-1988	Linsker	
HR	8.	5,360,948		11-01-1994	Thornberg	
HR	9.	5,618,744		04-08-1997	Suzuki et al.	
HR	10.	5,633,479		05-27-1997	Hirano	
HR	11.	5,650,653		07-22-1997	Rostoker et al.	
HR	12.	5,657,242		08-12-1997	Sekiyama et al.	
HR	13.	5,742,086		04-21-1998	Rostoker et al.	
HR	14.	5,777,360		07-07-1998	Rostoker et al.	
HR	15.	5,811,863		09-22-1998	Rostoker et al.	
HR	16.	5,914,887		06-22-1999	Scepanovic et al.	
HR	17.	5,973,376		10-26-1999	Rostoker et al.	
HR	18.	6,035,108		03-07-2000	Kikuchi	
HR	19.	6,128,767		10-03-2000	Chapman	
HR	20.	6,616,441		12-26-2000	Geryk	

Examiner Signature	<i>Yelena Rossoshnek</i>	Date Considered	03/15/04
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HR	21.	6,209,123	B1	03-27-2001	Maziasz et al.	
HR	22.	6,226,560	B1	05-01-2001	Hama	
HR	23.	6,262,487	B1	07-17-2001	Igarashi et al.	
HR	24.	6,301,686	B1	10-09-2001	Kikuchi et al.	
HR	25.	6,324,675	B1	11-27-2001	Dutta et al.	
HR	26.	6,342,682	B1	12-29-2002	Mori et al.	
HR	27.	6,385,758	B1	05-07-2002	Kikuchi et al.	
HR	28.	6,407,434	B1	06-18-2002	Rostoker et al.	
HR	29.	6,412,097	B1	06-25-2002	Kikuchi et al.	
HR	30.	6,436,804	B2	08-20-2002	Igarashi et al.	
HR	31.	6,512,292	B1	01-28-2003	Armbrust et al.	
HR	32.	6,615,400	B1	09-02-2003	Lukanc	

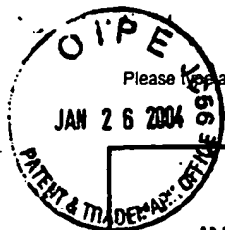
FOREIGN PATENT DOCUMENTS								
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		Office ³	Number ⁴	Kind Code (if known) ⁵				
HR	33.	JP	H03-173471	A	07-26-1991	Tawada et al.	with Japanese Patent Office's English translation of Abstract; and with English translation of the application.	✓
HR	34.	JP	04000677	A	01-06-1992	Fujiwara		
HR	35.	JP	H05-102305	A	04-23-1993	Akihiro Sato	with Japanese Patent Office's English translation of Abstract;	✓

Examiner Signature	<i>Michael R. Roshale</i>	Date Considered	03/15/04
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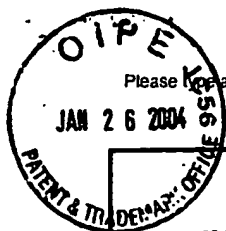
FOREIGN PATENT DOCUMENTS							
							and with English translation of the application.
HR	36.	JP	H05-243379	A	09-21-1993	Masako Kubota	with Japanese Patent Office's English translation of Abstract; and with English translation of the application. ✓
HR	37.	JP	H07-86407	A	03-31-1995	Shinpei Miura	with Japanese Patent Office's English translation of Abstract; and with English translation of the application. ✓
HR	38.	JP	H09-162279	A	06-20-1997	Masaaki Yoshida	with Japanese Patent Office's English translation of Abstract; and with English translation of the application. ✓

Examiner Signature	<i>Melvin Rosreshub</i>	Date Considered	03/15/04
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NON PATENT LITERATURE DOCUMENTS					
Examiner Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			T ²
HR	39.	Berger, B. et al., Nearly Optimal Algorithms and Bounds for Multilayer Channel Routing, Journal of the Association for Computing Machinery, pp. 500-542, March 1995.			
HR	40.	Chen, H., Routing L-Shaped Channels in Nonslicing-Structure Placement. 24 th ACM-IEEE Design Automation Conference, pp. 152-165, 1987.			
HR	41.	Cheng, K. et al., Manhattan or Non Manhattan? A Study of Alternative VLSI Routing Architectures, pp 47-52, 2000.			
HR	42.	Chiang, C. et al., Wirability of Knock-Knee Layouts with 45° Wires, IEEE Transactions on Circuits and Systems, Vol. 38, Issue 6, pp 613-624, June 1991.			
HR	43.	Overtone, G., EDA Underwriter 2 Finding Space in a Multi-Layer Board, Electronic Engineering, Morgan-Grampian LTD, vol. 67, no. 819, pp 29-30.			

Examiner Signature	<i>Heley Rosheshole</i>	Date Considered	03/15/04
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